DECLARATION FOR PATENT APPLICATION

As a below named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one individual inventor disclisted below) or an original first and joint inventor (if more than one individual inventor is listed below) of the invention in

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE SUPPRESSING A JUNCTION LEAK AND METHOD OF MANUFACTURING THE SAME

the specification of which is attached hereto unless the following box is checked.

Twas filed on January 27, 2000 as United States Application or PCT International Application No._______; and was amended on ______ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365 (a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

CountryCategoryApplication No.Filing DatePriority
ClaimJapanPatent11-016819January 26, 1999Yes

And I hereby appoint Paul N. Kokulis (Reg. No. 16, 773), Raymond F. Lippitt (Reg. No. 17,519), G. Lloyd Knight (Reg. No. 17,698), Carl G. Love (Reg. No. 18,781), Edgar H. Martin (Reg. No. 20,534), William K. West, Jr. (Reg. No. 22,057), Kevin E. Joyce (Reg. No. 20,508), George M. Sirilla (Reg. No. 22,429), David W. Brinkman (Reg. No. 20,817), Donald J. Bird (Reg. No. 25,323), Peter W. Gowdey (Reg. No. 25,872), Dale S. Lazar (Reg. No. 28,872), Paul E. White, Jr. (Reg. No. 32,011), Glenn J. Perry (Reg. No. 28,458), Kendrew H. Colton (Reg. No. 30,368), Michelle N. Lester (Reg. No. 32,331), G. Paul Edgell (Reg. No. 24,238), Lynn E. Eccleston (Reg. No. 35,861), Timothy J. Klima (Reg. No. 34,852), David A. Jakopin (Reg. No. 32,995), Mark G. Paulson (Reg. No. 30,793), Stephen C. Glazier (Reg. No. 31,361), Paul F. McQuade (Reg. No. 31,542), Ruth N. Morduch (Reg. No. 31,044), Richard H. Zaitlen (Reg. No. 27,248), Roger R. Wise (Reg. No. 31,044), Jay M. Finkelstein (Reg. No. 27,248), Roger R. Wise (Reg. No. 31,044), Jay M. Finkelstein (Reg. No. 21,082) and Anita M. Kirkpatrick (Reg. No. 32,617), each of whose address is 1100 New York Avenue, N.W., Ninth Floor, East Tower, Washington, D.C. 20005-3918, or any one of them, my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent & Trademark Office connected therewith, and request that correspondence be directed to The Cushman Darby & Cushman Intellectual Property Group of Pillsbury Madison & Sutro, LLP, 1100 New York Avenue, N.W., Ninth Floor, East Tower, Washington, D.C. 20005-3918.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION FOR PATENT APPLICATION

I declare further that my post office address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA. 1-1 Shibaura 1-chome, Minato-ku, Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

Inventor: (Signature)	Date	Residence
	Date: APR. 24.2000	
Jasukiro Tukaura	Citizen of: Japan	Yokohama-shi, Japan
Yasuhiro Fukaura		
	Date:	· -
	Citizen of: Japan	
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